·	Application No.	Applicant(s)
Notice of Allowability	10/751,398	AKASHI, TOMOKO
	Examiner	Art Unit
	Thao X. Le	2814
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	ars on the cover sheet water (OR REMAINS) CLOSED or other appropriate commitments. This application is	vith the correspondence address in this application. If not included . nunication will be mailed in due course. THIS
1. This communication is responsive to <u>01/06/04</u> .		
2. The allowed claim(s) is/are 1-18.		
3. The drawings filed on are accepted by the Examiner	·.	
 4. Acknowledgment is made of a claim for foreign priority un a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Applicat	ion No
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		le a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give		
 CORRECTED DRAWINGS (as "replacement sheets") mus (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date 040705. Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the 	on's Patent Drawing Revies Amendment / Comment 84(c)) should be written on	or in the Office action of the drawings in the front (not the back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 01/06/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Paper No 8), 7. ⊠ Examiner	Informal Patent Application (PTO-152) Summary (PTO-413), b./Mail Date is Amendment/Comment s Statement of Reasons for Allowance

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DETAILED ACTION

Drawings

1. Figure 6 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled 'Replacement Sheet' in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested: Flip Chip Ball Grid Array Package.

Reasons For Allowance

3. The following is an examiner's statement of reasons for allowance: Claims 1-18 are allowed because the prior art of record neither anticipated nor rendered obvious all the limitation of the base claims 1, 5, 8, and 12 including a capacitor-mounted board formed of an organic substrate ceramic substrate, in which through-hole wires are

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formed in the capacitor-mounted board, the through-hole wires electrically connect an upper surface to a back surface of the capacitor-mounted board, capacitors are mounted on the board in a manner that electrodes of capacitor-mounted the capacitors are connected to printed wires connected to the through-hole wires, pads connected to the through-hole wires are formed on a back surface and connected to the through-hole wires, and the capacitor-mounted board is flip chip connected to the semiconductor chip in a manner that the pads on the capacitor-mounted board are provided opposite to bumps on the back surface of the semiconductor chip, and an adhesive resin filled between the semiconductor chip and the package substrate and between the semiconductor chip and the capacitor-mounting board, the adhesive resin adhering the semiconductor chip to the package substrate and the semiconductor chip to the capacitor-mounting board.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the 4. examiner should be directed to Thao X. Le whose telephone number is (571) 272-1708. The examiner can normally be reached on M-F from 8:00 AM - 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael M. Fahmy can be reached on (571) 272 -1705. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thao X. Le 07 Apr. 2005

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PP MARY EXAMINER